
ZHOU FAN

UNDERGRADUATE IN COMPUTER SCIENCE

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Home Page: <https://zhoufan.me>

Github: <https://github.com/Evensgn>

Education

[Shanghai Jiao Tong University](#), China
Bachelor of Science(B.Sc.) in **Computer Science**

Sept 2016 ~ Present
June 2020 (expected)

Member of **ACM Honors Class**, which is an elite CS program for top 5% talented students.

GPA: **94.54 / 100** | Ranking: **1 / 41**

Research Interests

My current research interests lie in Machine Learning, particularly, **Reinforcement Learning** and **Deep Learning**.

Research Experience

[APEX Data & Knowledge Management Lab](#)
Shanghai Jiao Tong University

July 2018 ~ Present

Undergraduate Researcher

Advised by [Prof. Weinan Zhang](#) and [Prof. Yong Yu](#).

My current research topic is about reinforcement learning algorithms for various games.

Teaching Experiences

C++ Programming (A) (**CS151**)
Shanghai Jiao Tong University
https://acm.sjtu.edu.cn/wiki/PPCA_2018

Fall 2018 ~ Present

Teaching Assistant

- I instruct the TA teams of CS151, CS152 and CS358 in this semester.

C++ Programming (B) (**CS152**)
Shanghai Jiao Tong University
https://acm.sjtu.edu.cn/wiki/PPCA_2018

Fall 2018 ~ Present

Teaching Assistant

- I instruct the TA teams CS151, CS152 and CS358 in this semester.

Data Structures (A) (**CS358**)
Shanghai Jiao Tong University

Fall 2018 ~ Present

Teaching Assistant

- I instruct the TA teams of CS151, CS152 and CS358 in this semester.

Principle and Practice of Computer Algorithms
(**MS106**)
Shanghai Jiao Tong University

Summer 2018

https://acm.sjtu.edu.cn/wiki/PPCA_2018

Student Instructor

- Led the TA team (was one of the three lead TAs).
- Instructed a group of students to implement the [Chord distributed hash table](#) (DHT) in Golang, and to build a peer-to-peer application with the DHT implementation.

Data Structures (CS147)

Shanghai Jiao Tong University

https://acm.sjtu.edu.cn/wiki/Data_Structures_2018

Spring 2018

Teaching Assistant

- Led the TA team (was one of the three lead TAs).
- Prepared the course project (a railway ticket management system) with other TAs.
- Gave a lecture on web application development with Python, which was an optional way to implement the user interface of the course project.

C++ Programming (B) (CS152)

Shanghai Jiao Tong University

https://acm.sjtu.edu.cn/wiki/Programming_2017

Fall 2017

Teaching Assistant

- Led the TA team.
- Designed a new course project, which is to develop a Raiden-like game in C++.
- Gave lectures on basic algorithms and data structures.
- Designed/selected problems for programming assignments and exams.

Other Experiences

ACM Honors Class

Shanghai Jiao Tong University

<https://acm.sjtu.edu.cn/home>

Sept 2017 ~ Present

Server Administrator

Administrator of websites of ACM Class, including an [online judge system](#).

SJTUG (SJTU *NIX User Group)

Shanghai Jiao Tong University

Homepage: <https://sjtug.org>

GitHub: <https://github.com/sjtug>

June 2018 ~ Present

President

SJTUG is a Joyful Techie User Group in [Shanghai Jiao Tong University](#).

Discussions and talks take in the form of traditional [Linux User Groups](#)

that involves mailing lists and meet-ups. Topics range over projects, techniques, resources, problems, cool ideas, or just any cool stuff relevant to technology.

Honors and Awards

National Scholarship

Ministry of Education of P.R. China

2017

Han-Ying-Ju-Hua Scholarship

Shanghai Jiao Tong University

2017

Zhiyuan Honorary Scholarship

Shanghai Jiao Tong University

2016 & 2017

The 32nd China National Olympiad in Informatics (NOI)

Bronze Medal

July 2015

The 9th Asia and Pacific Informatics Olympiad (APIO) Silver Medal	May 2015
China Team Selection Competition for IOI 2016 (CTSC) Silver Medal	May 2015
National Olympiad in Informatics Winter Campus 2015 Second Prize	Feb 2015

*Selected
Projects*

EM Compiler - A Compiler for Mx* Language https://github.com/Evensgn/EM-Compiler It is a compiler implemented in Java from scratch, and the source language is Mx* , which is a C-and-Java-like language.	July 2018
RISC-V CPU https://github.com/Evensgn/RISC-V-CPU A RISC-V CPU with 5-stage pipeline, implemented in Verilog HDL.	Jan 2018
UTJS - Railway Ticket Management System https://github.com/Evensgn/UTJS Backend Development Group project for Data Structure 2017 (CS147), ACM Class, SJTU.	May 2017
AI of Chinese Battle Chess https://github.com/Evensgn/chinese-battle-chess-ai Course project of Programming 2016 (CS122), ACM Class, SJTU. Project Ranking: 1/48	Nov 2016
